ZL30159 General Purpose Clock Rate Converter

Short Form Data Sheet

March 2013

Features

- Precision synthesizer generates any clock-rate from 1 Hz to 177.5 MHz with jitter below 1ps
- Programmable digital PLL synchronize to any clock rate from 1 Hz (1 pps) to 750 MHz
- Input reference configurable as single ended LVCMOS (up to 177.5 MHz) or differential LVPECL (up to 750 MHz)
- Flexible two-stage architecture translates between arbitrary data rates, line coding rates and FEC rates
- Programmable Digital PLL loop filter: 30 MHz, 14 Hz, 28 Hz, 56 Hz, 112 Hz, 224 Hz, 448 Hz or 896 Hz
- Two LVCMOS outputs —from 1 Hz (1 pps) to 177.5 MHz

Ordering Information

ZL30159GGG2 64 Pin LBGA* Trays

*Pb Free Tin/Silver/Copper -40°C to +85°C

- Operates from a single crystal resonator or clock oscillator
- Configurable via SPI/I2C interface

Applications

- General purpose clock rate translator
- GPS receiver clock synthesizer

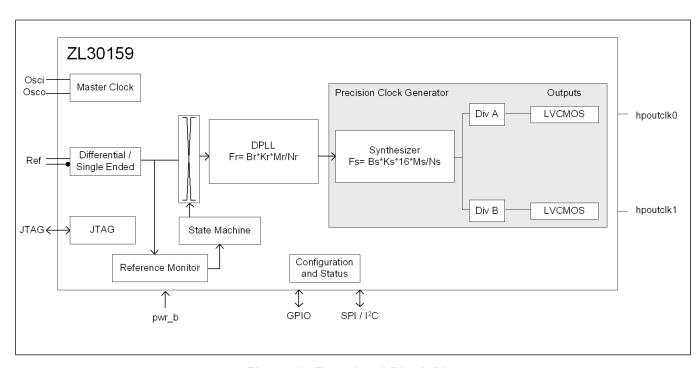
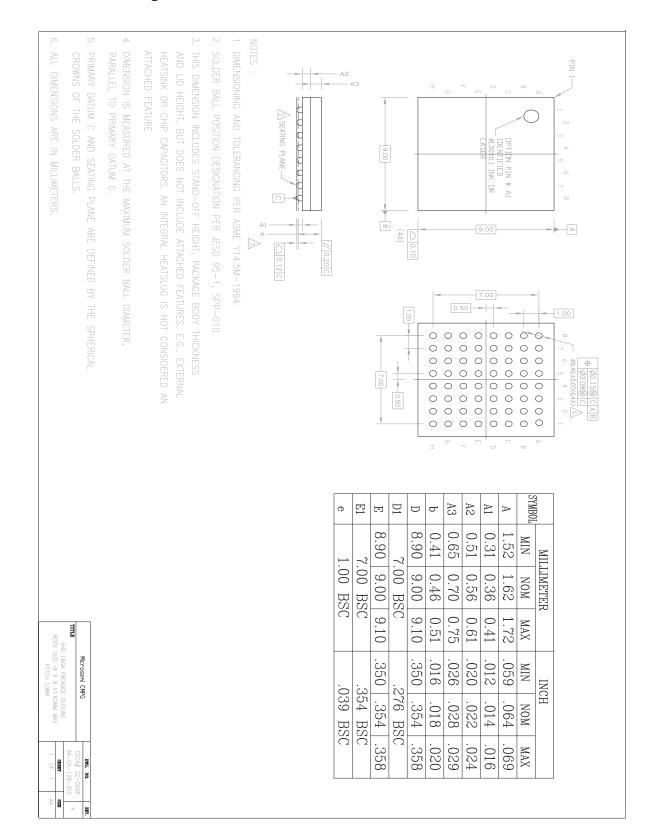


Figure 1 - Functional Block Diagram



Mechanical Drawing



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