

PHOTOCOUPLER **PS2732-1,PS2733-1**

HIGH COLLECTOR TO EMITTER VOLTAGE SOP MULTI PHOTOCOUPLER

-NEPOC Series-

DESCRIPTION

The PS2732-1 and PS2733-1 are optically coupled isolators containing a GaAs light emitting diode and an NPN silicon darlington-connected phototransistor.

This package is SOP (Small Outline Package) type and has shield effect to cut off ambient light. It is designed for high density mounting applications.

FEATURES

- High isolation voltage (BV = 2 500 Vr.m.s.)
- High collector to emitter voltage (VCEO = 300 V: PS2732-1)

(VCEO = 350 V: PS2733-1)

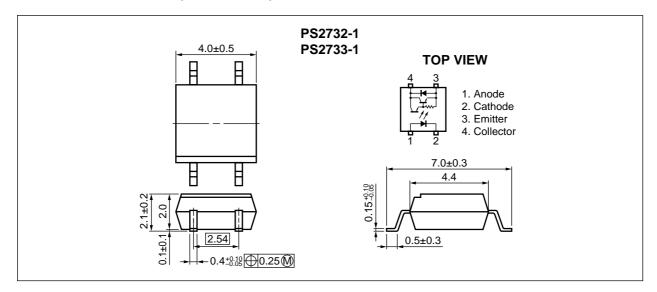
- · SOP (Small Outline Package) type
- High current transfer ratio (CTR = 4 000% TYP.)
- Ordering number of taping product: PS2732-1-F3, F4, PS2733-1-F3, F4
- Safety standards
 - UL approved: File No. E72422
 - BSI approved: File No. 8219/8220
 - CSA approved: File No. CA 101391
 - DIN EN60747-5-2 (VDE0884 Part2) approved (Option)

APPLICATIONS

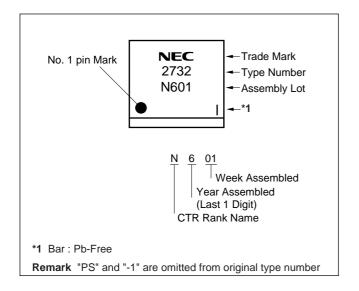
- Hybrid IC
- · Telephone/Telegraph Receiver
- FAX

The information in this document is subject to change without notice. Before using this document, please confirm that this is the latest version.

PACKAGE DIMENSIONS (in millimeters)



★ MARKING EXAMPLE



★ ORDERING INFORMATION

Part Number	Order Number	Solder Plating Specification	Packing Style	Safety Standard Approval	Application *1 Part Number
PS2732-1	PS2732-1-A	Pb-Free	Magazine case 100 pcs	Standard products	PS2732-1
PS2732-1-F3	PS2732-1-F3-A		Embossed Tape 3 500 pcs/reel	(UL, BSI, CSA	
PS2732-1-F4	PS2732-1-F4-A			approved)	
PS2732-1-V	PS2732-1-V-A		Magazine case 100 pcs	DIN EN60747-5-2	
PS2732-1-V-F3	PS2732-1-V-F3-A		Embossed Tape 3 500 pcs/reel	(VDE0884 Part2)	
PS2732-1-V-F4	PS2732-1-V-F4-A			Approved (Option)	
PS2733-1	PS2733-1-A		Magazine case 100 pcs	Standard products	PS2733-1
PS2733-1-F3	PS2733-1-F3-A		Embossed Tape 3 500 pcs/reel	(UL, BSI, CSA	
PS2733-1-F4	PS2733-1-F4-A			approved)	
PS2733-1-V	PS2733-1-V-A		Magazine case 100 pcs	DIN EN60747-5-2	
PS2733-1-V-F3	PS2733-1-V-F3-A		Embossed Tape 3 500 pcs/reel	(VDE0884 Part2)	
PS2733-1-V-F4	PS2733-1-V-F4-A			Approved (Option)	

^{*1} For the application of the Safety Standard, following part number should be used.

ABSOLUTE MAXIMUM RATINGS (TA = 25°C, unless otherwise specified)

Parameter		Symbol	Rati	Unit	
			PS2732-1	PS2733-1	
Diode	Forward Current (DC)	lF	50		mA
	Reverse Voltage	VR	6		٧
	Power Dissipation Derating	⊿P₀/°C	0.	.8	mW/°C
	Power Dissipation	Po	80		mW
	Peak Forward Current*1	IFP	1		А
Transistor	Collector to Emitter Voltage	Vceo	300	350	٧
	Emitter to Collector Voltage	VECO	0.3		٧
	Collector Current	Ic	150		mA
	Power Dissipation Derating	⊿Pc/°C	1.5		mW/°C
	Power Dissipation	Pc	150		mW
Isolation Voltage*2		BV	2 500		Vr.m.s.
Operating Ambient Temperature		TA	-55 to +100		°C
Storage Temperature		T _{stg}	-55 to +150		°C

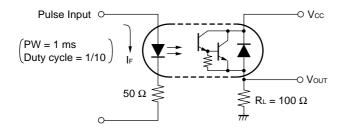
^{*1} PW = 100 μ s, Duty Cycle = 1%

^{*2} AC voltage for 1 minute at T_A = 25°C, RH = 60% between input and output Pins 1-2 shorted together, 3-4 shorted together.

ELECTRICAL CHARACTERISTICS (TA = 25 °C)

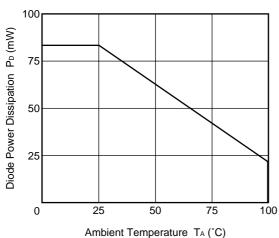
Parameter		Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Diode Forward Voltage		VF	IF = 10 mA		1.15	1.4	V
	Reverse Current	lr	V _R = 5 V			5	μА
	Terminal Capacitance	Ct	V = 0 V, f = 1 MHz		30		pF
Transistor	Collector to Emitter Dark Current	Iceo	IF = 0 mA, VcE = 300 V			400	nA
Coupled	Current Transfer Ratio (Ic/IF)	CTR	IF = 1 mA, VcE = 2 V	1 500	4 000		%
	Collector Saturation Voltage	VCE (sat)	I _F = 1 mA, I _C = 2 mA			1.0	V
	Isolation Resistance	R _{I-O}	Vi-o = 1 kVpc	10 ¹¹			Ω
	Isolation Capacitance	C _{I-O}	V = 0 V, f = 1 MHz		0.4		pF
	Rise Time *1	t r	$Vcc = 5 \text{ V}, \text{ Ic} = 10 \text{ mA}, \text{ RL} = 100 \Omega$		100		μS
	Fall Time *1	t f			100		

*1 Test circuit for switching time

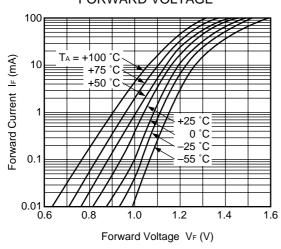


TYPICAL CHARACTERISTICS (TA = 25°C, unless otherwise specified)

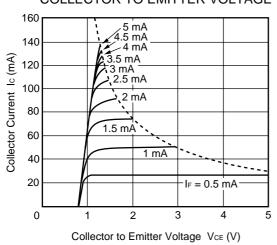




FORWARD CURRENT vs. FORWARD VOLTAGE

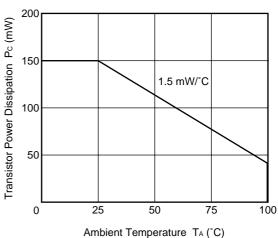


COLLECTOR CURRENT vs.
COLLECTOR TO EMITTER VOLTAGE

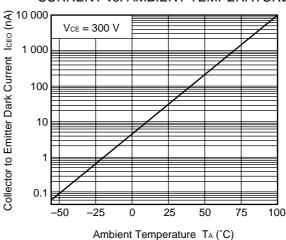


Remark The graphs indicate nominal characteristics.

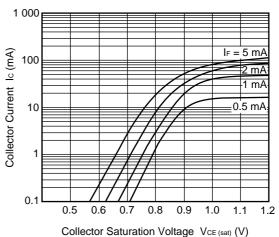
TRANSISTOR POWER DISSIPATION vs. AMBIENT TEMPERATURE



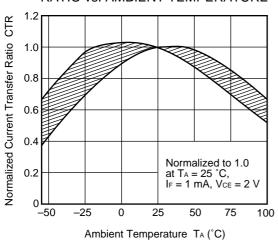
COLLECTOR TO EMITTER DARK CURRENT vs. AMBIENT TEMPERATURE



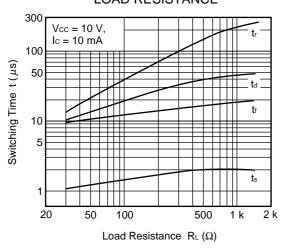
COLLECTOR CURRENT vs. COLLECTOR SATURATION VOLTAGE



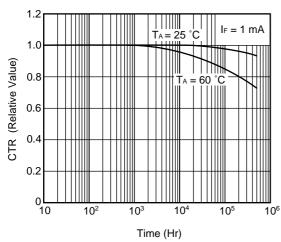
NORMALIZED CURRENT TRANSFER RATIO vs. AMBIENT TEMPERATURE



SWITCHING TIME vs. LOAD RESISTANCE

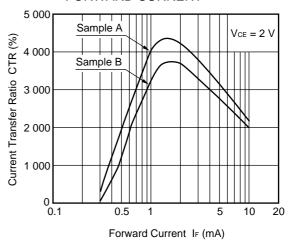


LONG TERM CTR DEGRADATION

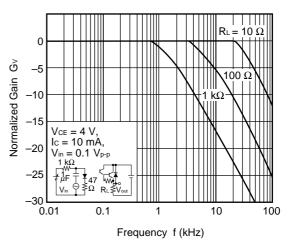


Remark The graphs indicate nominal characteristics.

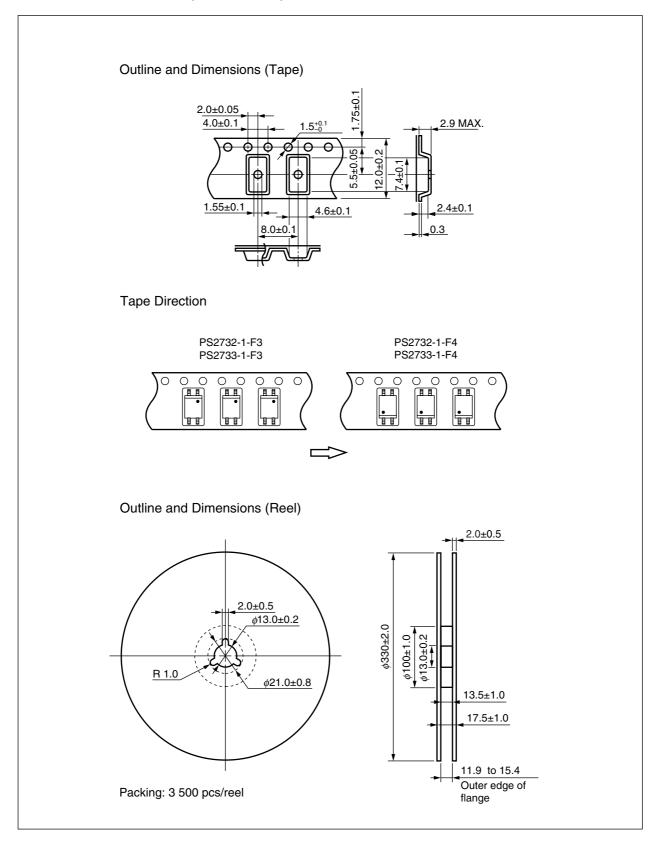
CURRENT TRANSFER RATIO vs. FORWARD CURRENT



FREQUENCY RESPONSE



TAPING SPECIFICATIONS (in millimeters)



NOTES ON HANDLING

1. Recommended soldering conditions

(1) Infrared reflow soldering

• Peak reflow temperature 260°C or below (package surface temperature)

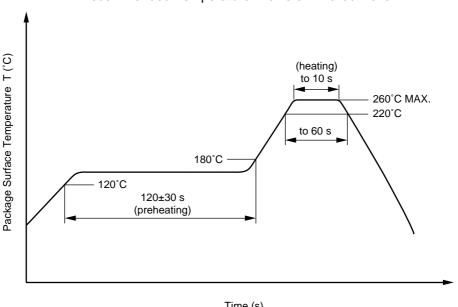
• Time of peak reflow temperature 10 seconds or less • Time of temperature higher than 220°C 60 seconds or less

• Time to preheat temperature from 120 to 180°C 120±30 s

· Number of reflows Three • Flux

Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

Recommended Temperature Profile of Infrared Reflow



Time (s)

(2) Wave soldering

• Temperature 260°C or below (molten solder temperature)

• Time 10 seconds or less

· Preheating conditions 120°C or below (package surface temperature)

· Number of times One (Allowed to be dipped in solder including plastic mold portion.)

• Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine

content of 0.2 Wt% is recommended.)

(3) Soldering by soldering iron

• Peak temperature (lead part temperature) 350°C or below • Time (each pins) 3 seconds or less

• Flux Rosin flux containing small amount of chlorine (The flux with a

maximum chlorine content of 0.2 Wt% is recommended.)

(a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead.

(b) Please be sure that the temperature of the package would not be heated over 100°C.

(4) Cautions

Fluxes

Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent.

2. Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output or between collector-emitters at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.

★ 3. Measurement conditions of current transfer ratios (CTR), which differ according to photocoupler

Check the setting values before use, since the forward current conditions at CTR measurement differ according to product.

When using products other than at the specified forward current, the characteristics curves may differ from the standard curves due to CTR value variations or the like. Therefore, check the characteristics under the actual operating conditions and thoroughly take variations or the like into consideration before use.

USAGE CAUTIONS

- 1. Protect against static electricity when handling.
- 2. Avoid storage at a high temperature and high humidity.

SPECIFICATION OF VDE MARKS LICENSE DOCUMENT (VDE0884)

Parameter	Symbol	Speck	Unit
Application classification (DIN VDE 0109)			
for rated line voltages ≤ 150 Vr.m.s.		IV	
for rated line voltages \leq 300 Vr.m.s.		III	
Climatic test class (DIN IEC 68 Teil 1/09.80)		55/100/21	
Dielectric strength			
Maximum operating isolation voltage	UIORM	710	V_{peak}
Test voltage (partial discharge test, procedure a for type test and random test)	U_pr	850	V_{peak}
$U_{pr} = 1.2 \times U_{IORM}$, $P_d < 5 pC$			
Test voltage (partial discharge test, procedure b for all devices test) $U_{pr} = 1.6 \times U_{IORM}, P_d < 5 pC$	Upr	1 140	V _{peak}
Highest permissible overvoltage	Utr	4 000	V _{peak}
Degree of pollution (DIN VDE 0109)		2	
Clearance distance		> 5	mm
Creepage distance		> 5	mm
Comparative tracking index (DIN IEC 112/VDE 0303 part 1)	СТІ	175	
Material group (DIN VDE 0109)		III a	
Storage temperature range	Tstg	-55 to +150	°C
Operating temperature range	TA	-55 to +100	°C
Isolation resistance, minimum value			
$V_{10} = 500 \text{ V dc at T}_A = 25 ^{\circ}\text{C}$	Ris MIN.	10 ¹²	Ω
V _{IO} = 500 V dc at T _A MAX. at least 100 °C	Ris MIN.	10 ¹¹	Ω
Safety maximum ratings (maximum permissible in case of fault, see thermal derating curve)			
Package temperature	Tsi	150	°C
Current (input current I _F , Psi = 0)	Isi	300	mA
Power (output or total power dissipation)	Psi	500	mW
Isolation resistance			
$V_{IO} = 500 \text{ V dc at T}_A = 175 ^{\circ}\text{C (Tsi)}$	Ris MIN.	10 ⁹	Ω

When the product(s) listed in this document is subject to any applicable import or export control laws and regulation of the authority having competent jurisdiction, such product(s) shall not be imported or exported without obtaining the import or export license.

- The information in this document is current as of March, 2006. The information is subject to change
 without notice. For actual design-in, refer to the latest publications of NEC's data sheets or data
 books, etc., for the most up-to-date specifications of NEC semiconductor products. Not all products
 and/or types are available in every country. Please check with an NEC sales representative for
 availability and additional information.
- No part of this document may be copied or reproduced in any form or by any means without prior written consent of NEC. NEC assumes no responsibility for any errors that may appear in this document.
- NEC does not assume any liability for infringement of patents, copyrights or other intellectual property rights of
 third parties by or arising from the use of NEC semiconductor products listed in this document or any other
 liability arising from the use of such products. No license, express, implied or otherwise, is granted under any
 patents, copyrights or other intellectual property rights of NEC or others.
- Descriptions of circuits, software and other related information in this document are provided for illustrative purposes in semiconductor product operation and application examples. The incorporation of these circuits, software and information in the design of customer's equipment shall be done under the full responsibility of customer. NEC assumes no responsibility for any losses incurred by customers or third parties arising from the use of these circuits, software and information.
- While NEC endeavours to enhance the quality, reliability and safety of NEC semiconductor products, customers
 agree and acknowledge that the possibility of defects thereof cannot be eliminated entirely. To minimize
 risks of damage to property or injury (including death) to persons arising from defects in NEC
 semiconductor products, customers must incorporate sufficient safety measures in their design, such as
 redundancy, fire-containment, and anti-failure features.
- NEC semiconductor products are classified into the following three quality grades:
 - "Standard", "Special" and "Specific". The "Specific" quality grade applies only to semiconductor products developed based on a customer-designated "quality assurance program" for a specific application. The recommended applications of a semiconductor product depend on its quality grade, as indicated below. Customers must check the quality grade of each semiconductor product before using it in a particular application.
 - "Standard": Computers, office equipment, communications equipment, test and measurement equipment, audio and visual equipment, home electronic appliances, machine tools, personal electronic equipment and industrial robots
 - "Special": Transportation equipment (automobiles, trains, ships, etc.), traffic control systems, anti-disaster systems, anti-crime systems, safety equipment and medical equipment (not specifically designed for life support)
 - "Specific": Aircraft, aerospace equipment, submersible repeaters, nuclear reactor control systems, life support systems and medical equipment for life support, etc.

The quality grade of NEC semiconductor products is "Standard" unless otherwise expressly specified in NEC's data sheets or data books, etc. If customers wish to use NEC semiconductor products in applications not intended by NEC, they must contact an NEC sales representative in advance to determine NEC's willingness to support a given application.

(Note)

- (1) "NEC" as used in this statement means NEC Corporation, NEC Compound Semiconductor Devices, Ltd. and also includes its majority-owned subsidiaries.
- (2) "NEC semiconductor products" means any semiconductor product developed or manufactured by or for NEC (as defined above).

M8E 00.4-0110

Caution

GaAs Products

This product uses gallium arsenide (GaAs).

GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.

- Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
 - Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
- 2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.
- Do not burn, destroy, cut, crush, or chemically dissolve the product.
- Do not lick the product or in any way allow it to enter the mouth.

▶ For further information, please contact

NEC Compound Semiconductor Devices, Ltd. http://www.ncsd.necel.com/

E-mail: salesinfo@ml.ncsd.necel.com (sales and general) techinfo@ml.ncsd.necel.com (technical)

Sales Division TEL: +81-44-435-1573 FAX: +81-44-435-1579

NEC Compound Semiconductor Devices Hong Kong Limited

E-mail: ncsd-hk@elhk.nec.com.hk (sales, technical and general)

Hong Kong Head Office TEL: +852-3107-7303 FAX: +852-3107-7309
Taipei Branch Office TEL: +886-2-8712-0478 FAX: +886-2-2545-3859
Korea Branch Office TEL: +82-2-558-2120 FAX: +82-2-558-5209

NEC Electronics (Europe) GmbH http://www.ee.nec.de/

TEL: +49-211-6503-0 FAX: +49-211-6503-1327

California Eastern Laboratories, Inc. http://www.cel.com/

TEL: +1-408-988-3500 FAX: +1-408-988-0279