## MCH6437

## Power MOSFET

20V, 24m , 7A, Single N-Channel
ON Semiconductor ${ }^{\circledR}$
www.onsemi.com

| VDSS | RDS(on) Max | ID Max |
| :---: | :---: | :---: |
| 20 V | $24 \mathrm{~m} \Omega @ 4.5 \mathrm{~V}$ |  |
|  | $35 \mathrm{~m} \Omega @ 2.5 \mathrm{~V}$ | 7 A |
|  | $65 \mathrm{~m} \Omega @ 1.8 \mathrm{~V}$ |  |

- ESD Diode-Protected Gate
- Pb-Free and RoHS Compliance


## Specifications

Absolute Maximum Ratings at $\mathrm{Ta}=25^{\circ} \mathrm{C}$

| Parameter | Symbol | Value | Unit |
| :--- | :--- | ---: | :---: |
| Drain to Source Voltage | VDSS | 20 | V |
| Gate to Source Voltage | VGSS | $\pm 12$ | V |
| Drain Current (DC) | ID | 7 | A |
| Drain Current (Pulse) <br> PW $\leq 10 \mu \mathrm{~s}$, duty cycle $\leq 1 \%$ | IDP | 28 | A |
| Power Dissipation <br> When mounted on ceramic substrate <br> $\left(1200 \mathrm{~mm}^{2} \times 0.8 \mathrm{~mm}\right)$ | PD | 1.5 | W |
| Junction Temperature | Tj | 150 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature | Tstg | -55 to +150 | ${ }^{\circ} \mathrm{C}$ |

## Thermal Resistance Ratings

| Parameter | Symbol | Value | Unit |
| :--- | :--- | :--- | :---: |
| Junction to Ambient <br> When mounted on ceramic substrate <br> $\left(1200 \mathrm{~mm}^{2} \times 0.8 \mathrm{~mm}\right)$ | $R_{\theta J A}$ | 83.3 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |

Electrical Connection
N -Channel


Packing Type : TL
Marking


Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

## ORDERING INFORMATION

See detailed ordering and shipping information on page 5 of this data sheet.

MCH6437
Electrical Characteristics at $\mathrm{Ta}=25^{\circ} \mathrm{C}$

| Parameter | Symbol | Conditions | Value |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | min | typ | max |  |
| Drain to Source Breakdown Voltage | V (BR) DSS | $\mathrm{I}_{\mathrm{D}}=1 \mathrm{~mA}, \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V}$ | 20 |  |  | V |
| Zero-Gate Voltage Drain Current | IDSS | $\mathrm{V}_{\mathrm{DS}}=20 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V}$ |  |  | 1 | $\mu \mathrm{A}$ |
| Gate to Source Leakage Current | IGSS | $\mathrm{V}_{\mathrm{GS}}= \pm 8 \mathrm{~V}, \mathrm{~V}_{\mathrm{DS}}=0 \mathrm{~V}$ |  |  | $\pm 10$ | $\mu \mathrm{A}$ |
| Gate Threshold Voltage | $\mathrm{V}_{\mathrm{GS}}(\mathrm{th})$ | $\mathrm{V}_{\mathrm{DS}}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=1 \mathrm{~mA}$ | 0.4 |  | 1.3 | V |
| Forward Transconductance | gFS | $V_{D S}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=4 \mathrm{~A}$ |  | 6.2 |  | S |
| Static Drain to Source On-State Resistance | R DS(on)1 | $\mathrm{I}_{\mathrm{D}}=4 \mathrm{~A}, \mathrm{~V}_{\mathrm{GS}}=4.5 \mathrm{~V}$ |  | 18 | 24 | $\mathrm{m} \Omega$ |
|  | R ${ }_{\text {DS }}(\mathrm{on}) 2$ | $\mathrm{I}_{\mathrm{D}}=2 \mathrm{~A}, \mathrm{~V}_{\mathrm{GS}}=2.5 \mathrm{~V}$ |  | 25 | 35 | $\mathrm{m} \Omega$ |
|  | R ${ }_{\text {DS }}$ (on)3 | $\mathrm{I}_{\mathrm{D}}=1 \mathrm{~A}, \mathrm{~V}_{\mathrm{GS}}=1.8 \mathrm{~V}$ |  | 38 | 65 | $\mathrm{m} \Omega$ |
| Input Capacitance | Ciss | $V_{D S}=10 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ |  | 660 |  | pF |
| Output Capacitance | Coss |  |  | 125 |  | pF |
| Reverse Transfer Capacitance | Crss |  |  | 100 |  | pF |
| Turn-ON Delay Time | $\mathrm{t}_{\mathrm{d}}(\mathrm{on})$ | See specified Test Circuit |  | 9.7 |  | ns |
| Rise Time | $\mathrm{tr}_{r}$ |  |  | 53 |  | ns |
| Turn-OFF Delay Time | $\mathrm{t}_{\mathrm{d}}$ (off) |  |  | 72 |  | ns |
| Fall Time | $\mathrm{tf}^{\text {f }}$ |  |  | 65 |  | ns |
| Total Gate Charge | Qg | $\mathrm{V}_{\mathrm{DS}}=10 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=4.5 \mathrm{~V}, \mathrm{I}=7 \mathrm{~A}$ |  | 8.4 |  | nC |
| Gate to Source Charge | Qgs |  |  | 1.0 |  | nC |
| Gate to Drain "Miller" Charge | Qgd |  |  | 2.4 |  | nC |
| Forward Diode Voltage | VSD | $\mathrm{IS}=7 \mathrm{~A}, \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V}$ |  | 0.81 | 1.2 | V |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

## Switching Time Test Circuit





PD - Ta

$\mathrm{R}_{\theta \mathrm{JA}}$ - Pulse Time


## Package Dimensions

MCH6437-TL-E / MCH6437-TL-W

## MCPH6

CASE 419AS
ISSUE O
unit : mm

1 : Drain
2 : Drain
3 : Gate
4 : Source
5 : Drain
6 : Drain



Recommended
Soldering Footprint


ORDERING INFORMATION

| Device | Package | Shipping | Note |
| :--- | :---: | :---: | :---: |
| MCH6437-TL-E | MCPH6 | $3,000 \mathrm{pcs}$ / Tape \& Reel | Pb-Free |
| MCH6437-TL-W | SC-88FL,SC-70-6,SOT-363 |  | Pb-Free <br> and Halogen Free |

$\dagger$ For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D. http://www.onsemi.com/pub_link/Collateral/BRD8011-D.PDF

Note on usage : Since the MCH6437 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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